



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/652,066	08/29/2003	Tim Murphy	501039.04	9772

7590 06/16/2004

Steven H. Arterberry, Esq.
DORSEY & WHITNEY LLP
Suite 3400
1420 Fifth Avenue
Seattle, WA 98101

EXAMINER

IM, JUNGHWA M

ART UNIT PAPER NUMBER

2811

DATE MAILED: 06/16/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/652,066

Applicant(s)

MURPHY ET AL.

Examiner

Junghwa M. Im

Art Unit

2811

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 29 March 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-54 is/are pending in the application.
- 4a) Of the above claim(s) 1-49 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 50-54 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>06/09/2004</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Election/Restrictions

During a telephone conversation with Edward Bulchis on May 19, 2004 a provisional election was made without traverse to prosecute claim 50-54. Affirmation of this election must be made by applicant in replying to this Office action. Claims 1-49 withdrawn from further consideration by the examiner, 37 CFR 1.142(b), as being drawn to a non-elected invention.

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 50-54 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 50 recites unclear limitations of “the first converter operable to data signals on the data bus and convert the data signals into corresponding data output electromagnetic waves, and operable to receive address, data, and control electromagnetic waves and convert these electromagnetic waves into corresponding electric address, data, and control signals that are applied on the address, data, and control busses” and “the converter operable to receive electric address, data, and control signals on corresponding conductors and to convert these electric signals into corresponding address, data, and control electromagnetic waves that are communicated to the first converter.” These limitations imply that the first converter converts

Art Unit: 2811

the electric signal into EM wave and at the same time converting EM waves back to the electric signal, and further, the converted EM waves being communicated to the first converter.

In addition, there is an unclear limitation such as "address, data, and control electromagnetic waves." Note that address and data and control circuits work in corporation to process data/information (an electric signal) in a memory device as shown in Fig. 6 of the specification.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 50, 51, 53 and 54 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nhu (US 5237441) in view of Sudo (US 5198684).

Regarding claim 50, insofar as understood, Fig. 2 of Nhu shows a computer system, comprising:

a data input device (connected to an input terminal 16);

a data output device (connected to an output terminal 18);

a processor (80, 82) coupled to the data input and output devices; and memory device (42) including a chip package (20 in Fig. 4) having a plurality of conductors (24, 26 in Fig. 4) coupled to the processor, the memory device the memory device including,

a chip including memory circuitry, the memory circuitry including,

Art Unit: 2811

an address decoder (100) coupled to an address bus;
a read/write circuit (94, 96, 98) coupled to a data bus;
a control circuit (92) coupled to a control bus;
a memory-cell array (90) coupled to the address decoder, control circuit, and read/write circuit, the memory-cell array; and
a chip package (20) physically coupled to the chip.

Nhu shows the most aspect of the instant invention except that a first convert in the memory circuit to convert the corresponding EM signals to electric signals (and visa versa) and a second convert in the chip package to receive the converted signals from the first converter converting back.

Fig. 1 of Sudo shows a light transmit-receiving elements (40A, 40B) with two converters (42B, 44B) formed on chip package (10). And Sudo further discloses that the first converter (44B) converts the electric signal to an optical signal (EM signal) and visa versa and the converted signals coupled to the second converter (42B) while the second converter converting the EM signal to an electrical signal to the IC circuit on the package substrate (col. 6, lines 33-65).

It would have been obvious to one of ordinary skill in the art at the time of the invention was made to utilize the teachings of Sudo into the device of Nhu in order to have “a first converter coupled to the address, data, and control busses, the first converter operable to data signals on the data bus and convert the data signals into corresponding data output electromagnetic waves, and operable to receive address, data, and control electromagnetic waves and convert these electromagnetic waves into corresponding electric address, data, and control

Art Unit: 2811

signals that are applied on the address, data, and control busses, respectively; and a chip package physically coupled to the chip, the chip package including a second converter that is operable to receive the data output electromagnetic waves from the first converter and convert these received electromagnetic waves into corresponding electric data output signals that are applied to corresponding conductors, and the second converter operable to receive electric address, data, and control signals on corresponding conductors and to convert these electric signals into corresponding address, data, and control electromagnetic waves that are communicated to the first converter” since such a configuration improves a integration density of the IC (col. 1, lines 22-30 of Nhu’s specification).

Regarding claim 51, Nhu discloses the electromagnetic waves comprise optical electromagnetic signals (col. 1, lines 38-40).

Regarding claim 53, Sudo discloses the first and second converters comprise laser diodes (col. 6, lines 54-59).

Regarding claim 54, the combined teachings of Nhu and Sudo fail to disclose that “the memory device comprises a dynamic random access memory.” However, it would have been obvious to one of ordinary skill in the art at the time of the invention was made to use a DRAM for the memory device of Nhu and Sudo since DRAM memory chips consume less power than other kind of memory chips.

Claim 52 is rejected under 35 U.S.C. 103(a) as being unpatentable over Nhu and Sudo as applied to claim 1 above, and further in view of Austin et al. (US 5200631), herein after Austin.

Art Unit: 2811

Claim 52, the combined teachings of Nhu and Sudo shows the most aspect of the instant invention except "the electromagnetic waves comprise infrared electromagnetic waves." Austin discloses a semiconductor package wherein the electromagnetic waves comprise infrared electromagnetic waves (col. 4, line 68 - col. 5, line 1).

It would have been obvious to one of ordinary skill in the art at the time of the invention was made to include a infrared EM wave application to the device of Nhu and Sudo with the teachings of Austin to broaden the range of the device application.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Junghwa M. Im whose telephone number is (571) 272-1655. The examiner can normally be reached on MON.-FRI. 8:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C Lee can be reached on (571) 272-1732. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

jmi

Steven Loke
Primary Examiner

